

PRIOR ART

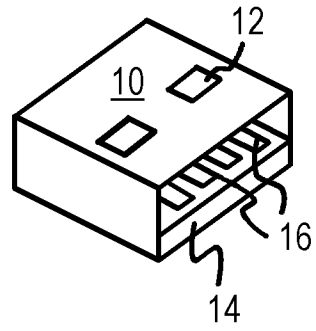
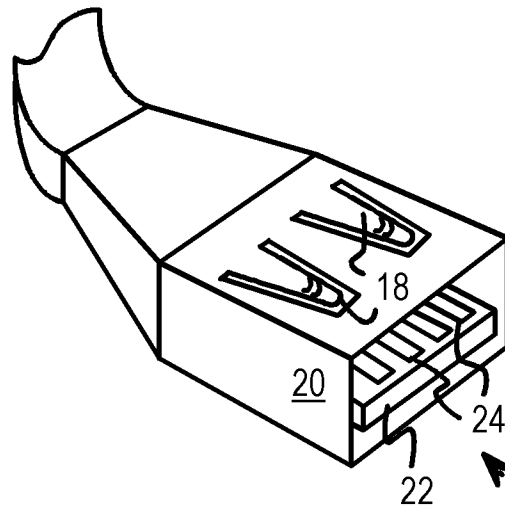


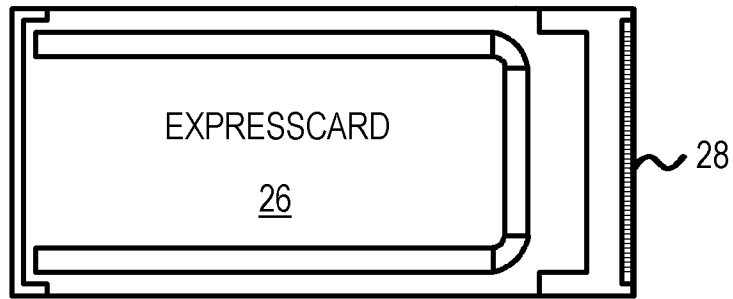
FIG. 1A



PRIOR ART

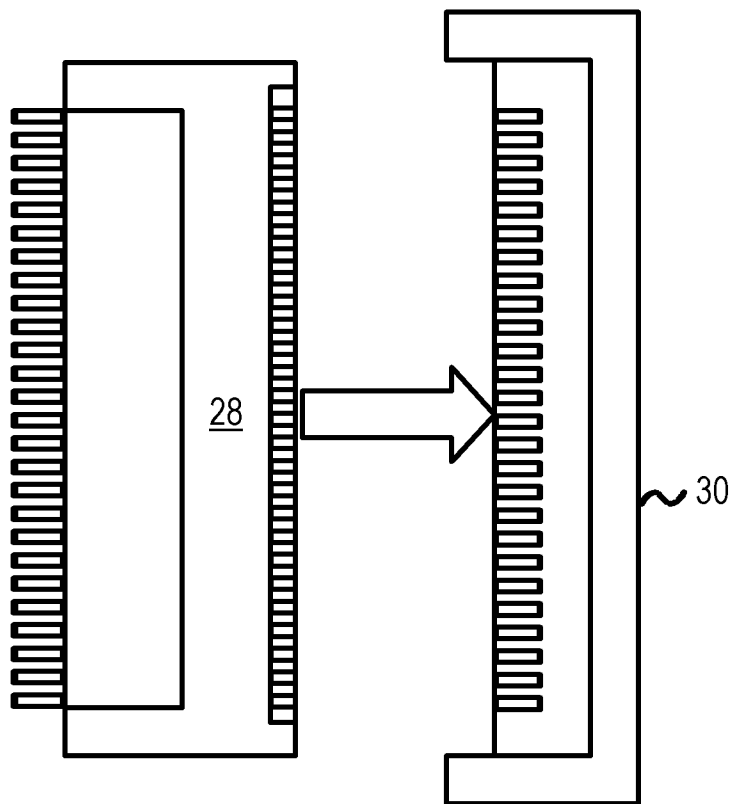
FIG. 1B





PRIOR ART

FIG. 2A



PRIOR ART

FIG. 2B

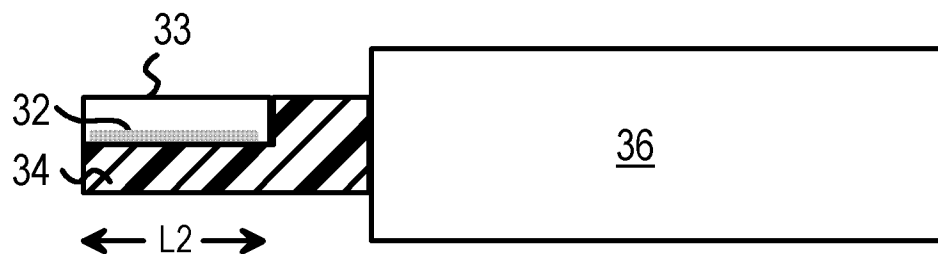


FIG. 3A PRIOR ART

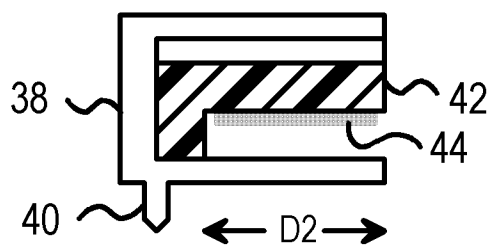
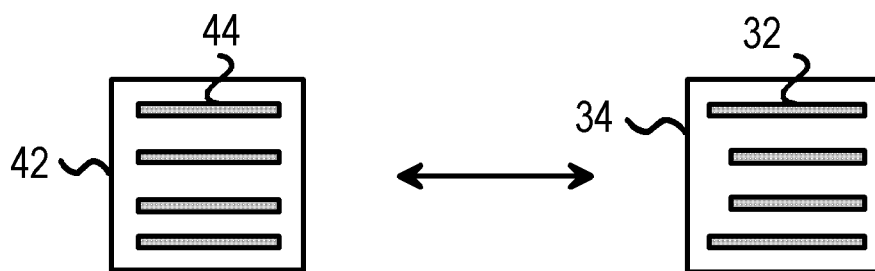


FIG. 3B PRIOR ART



PRIOR ART

FIG. 3C

FIG. 3D

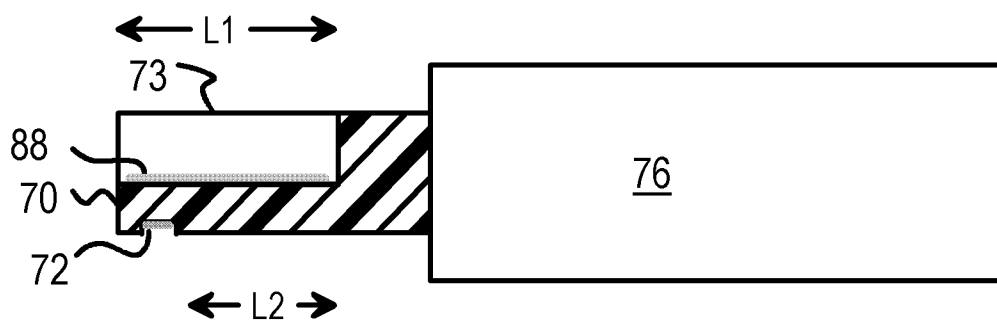


FIG. 4A

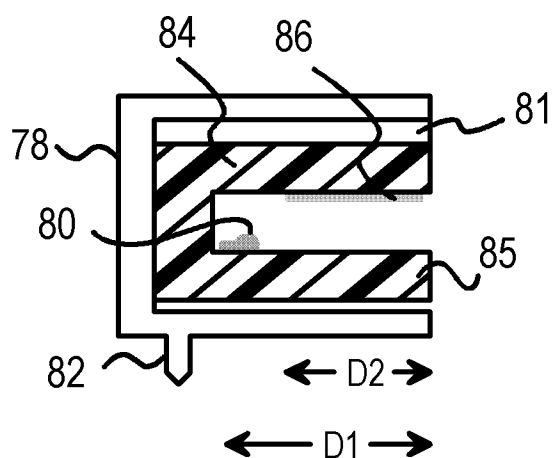


FIG. 4B

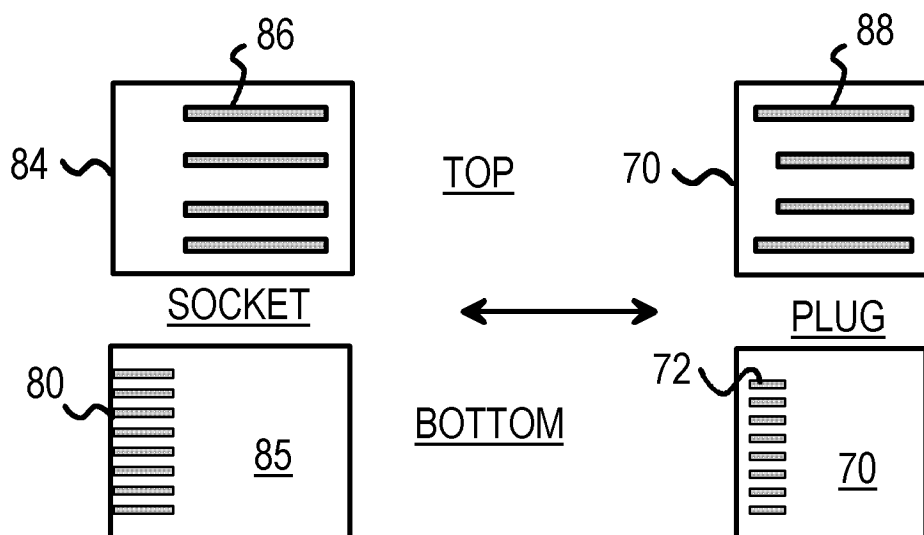


FIG. 4C

FIG. 4D

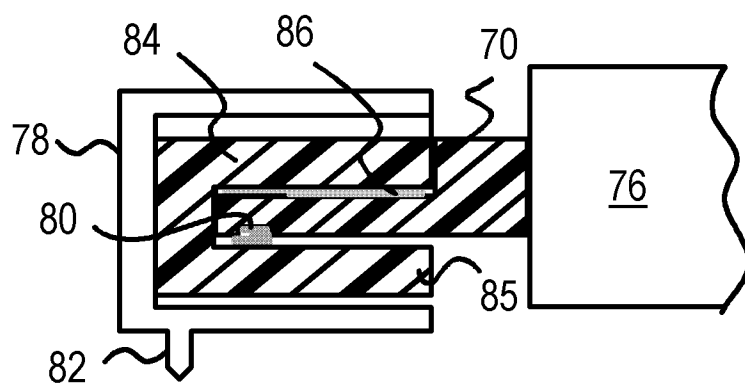


FIG. 4E

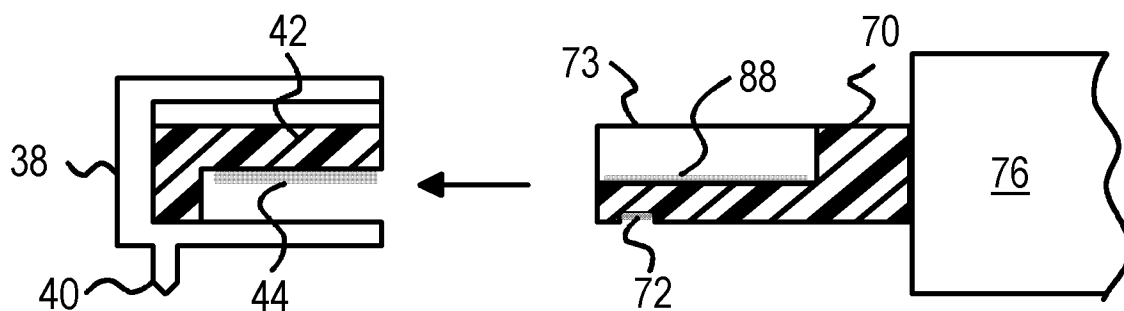


FIG. 4F

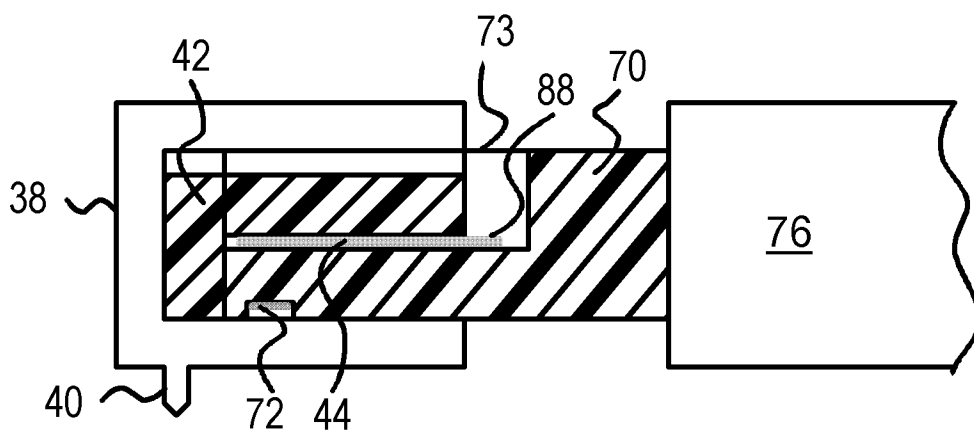


FIG. 4G

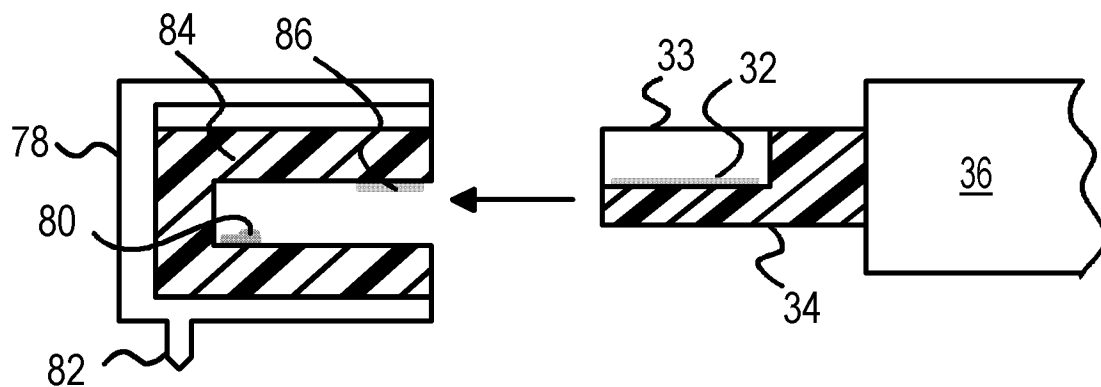


FIG. 4H

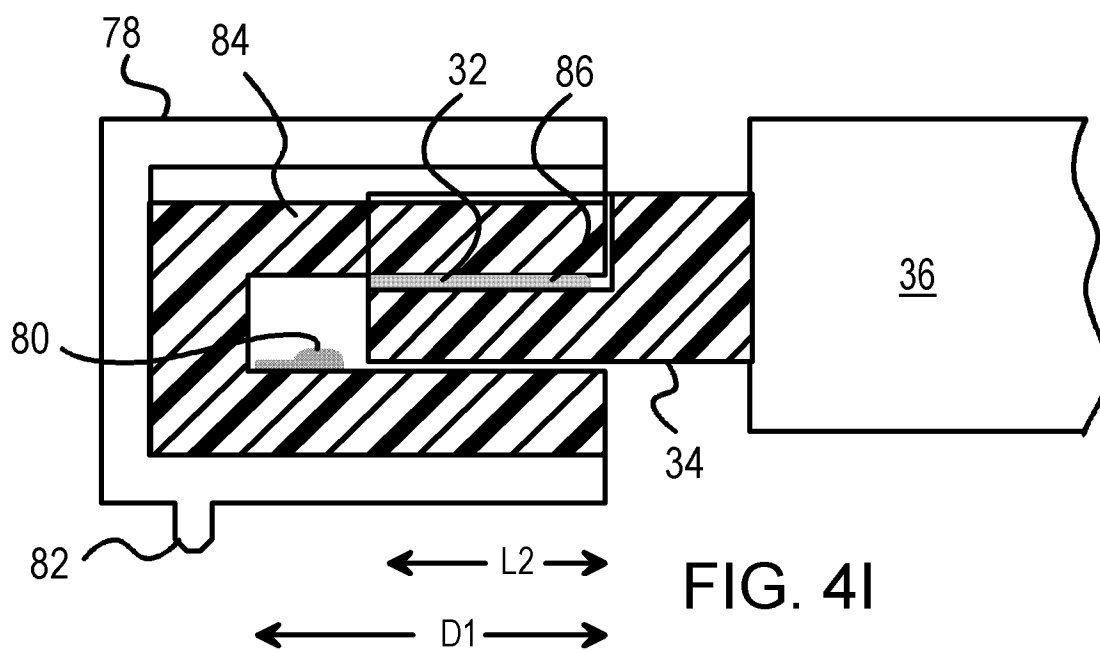


FIG. 4I

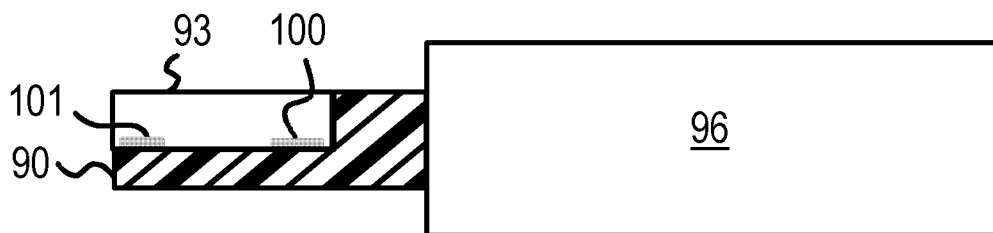


FIG. 5A

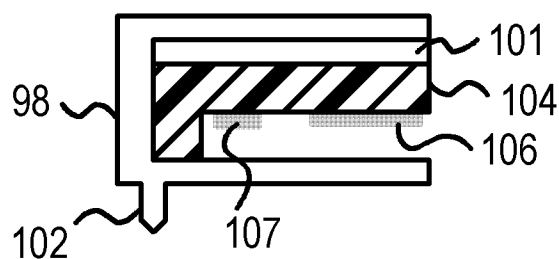


FIG. 5B

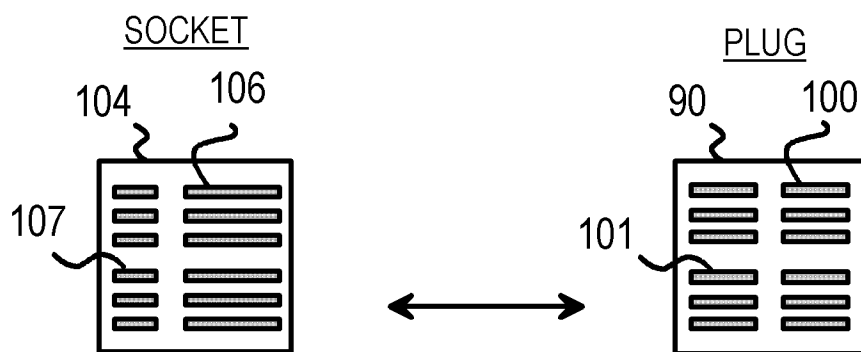


FIG. 5C

FIG. 5D

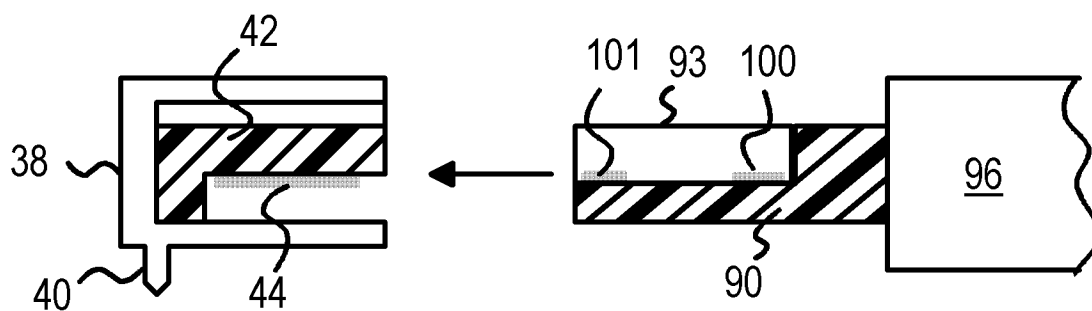


FIG. 5E

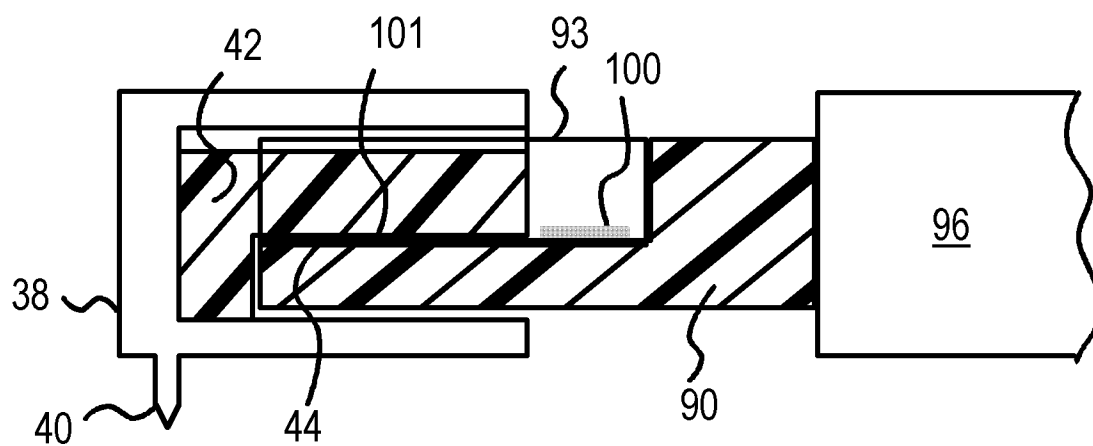


FIG. 5F

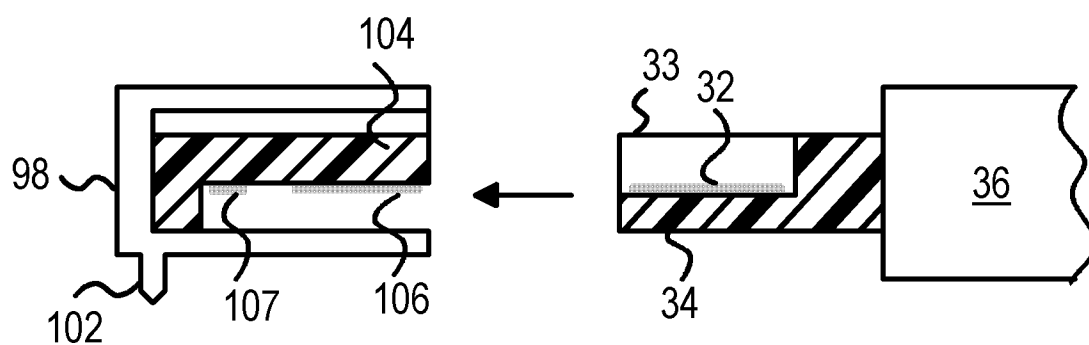


FIG. 5G

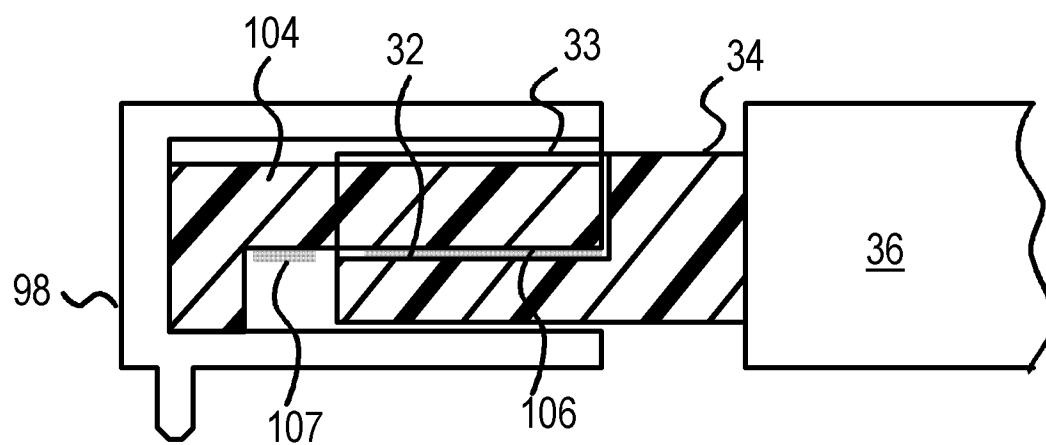


FIG. 5H

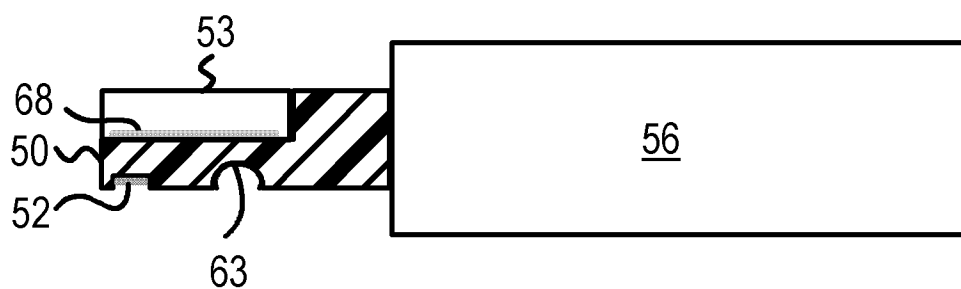


FIG. 6A

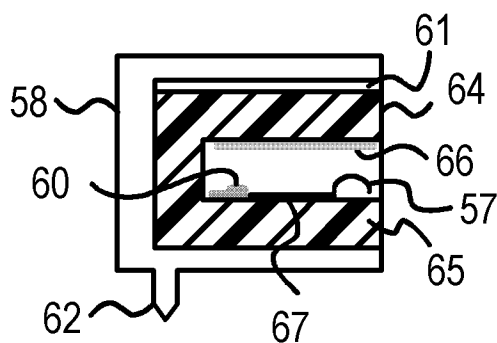


FIG. 6B

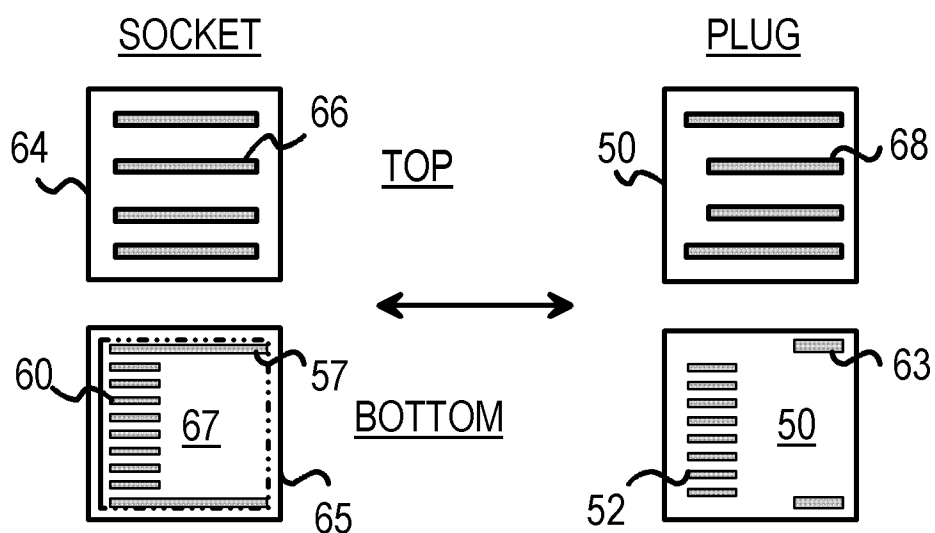


FIG. 6C

FIG. 6D

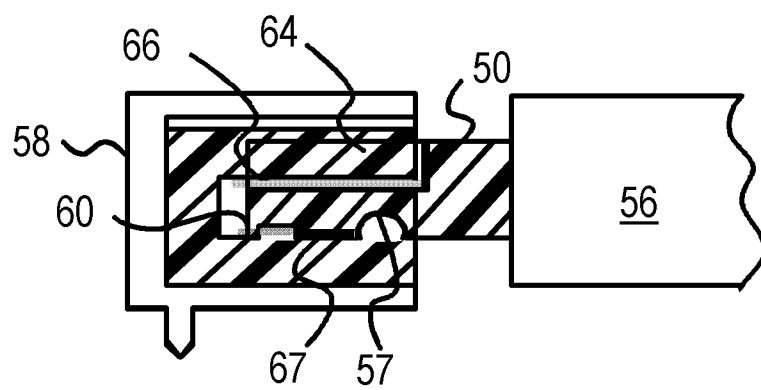


FIG. 6E

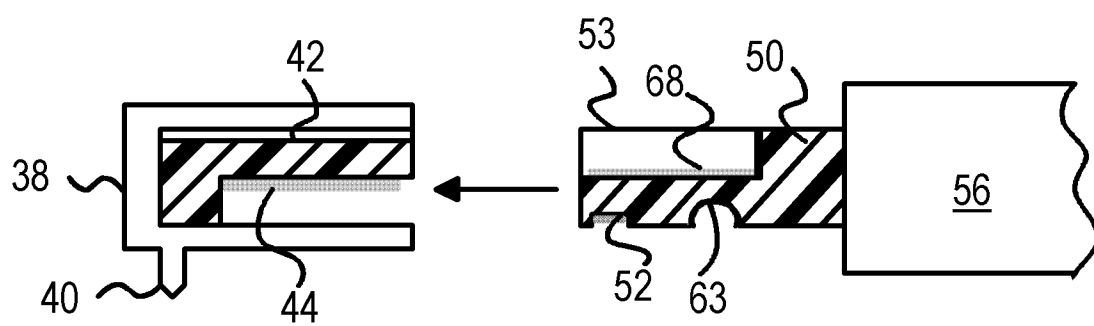


FIG. 6F

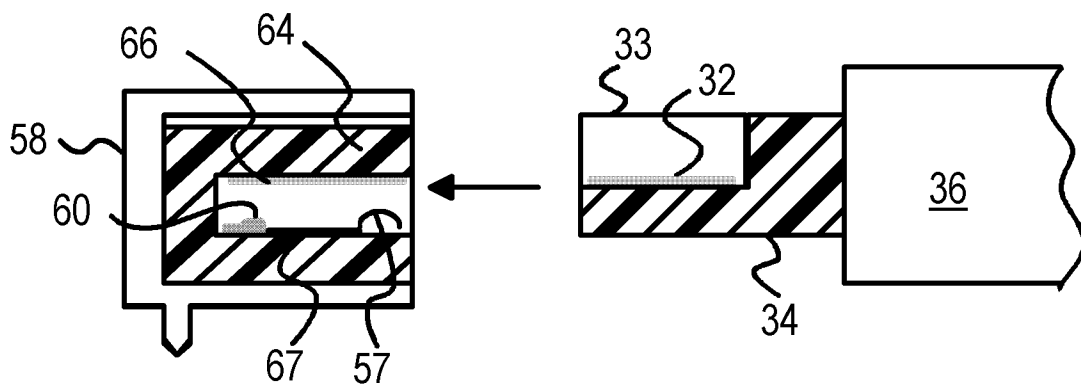


FIG. 6G

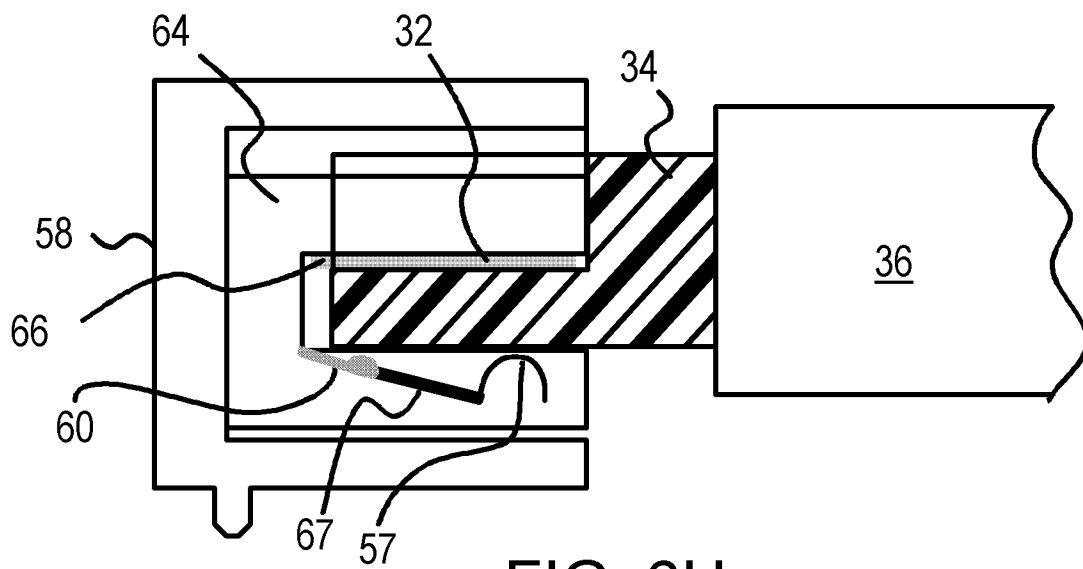


FIG. 6H

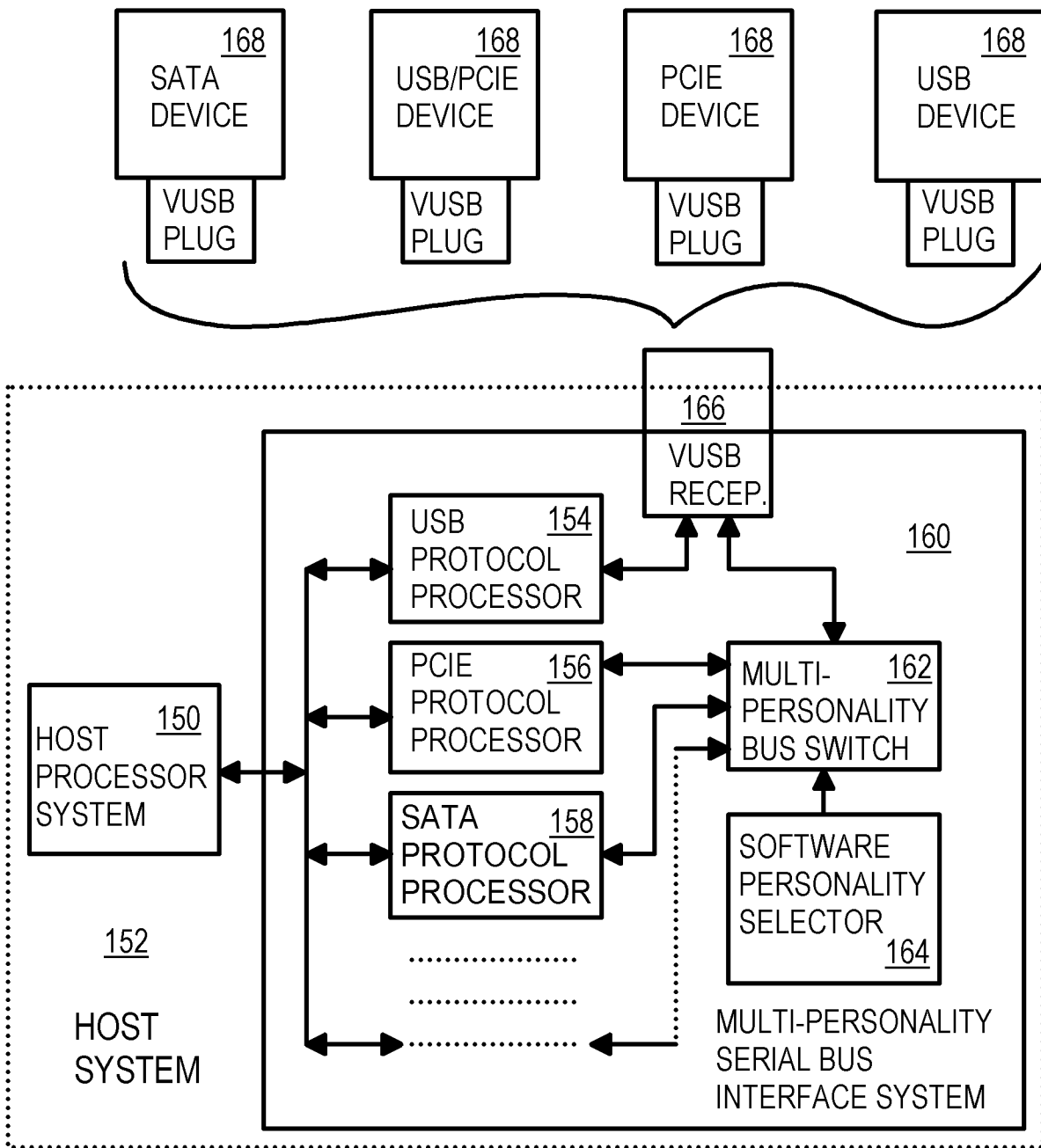


FIG. 7A

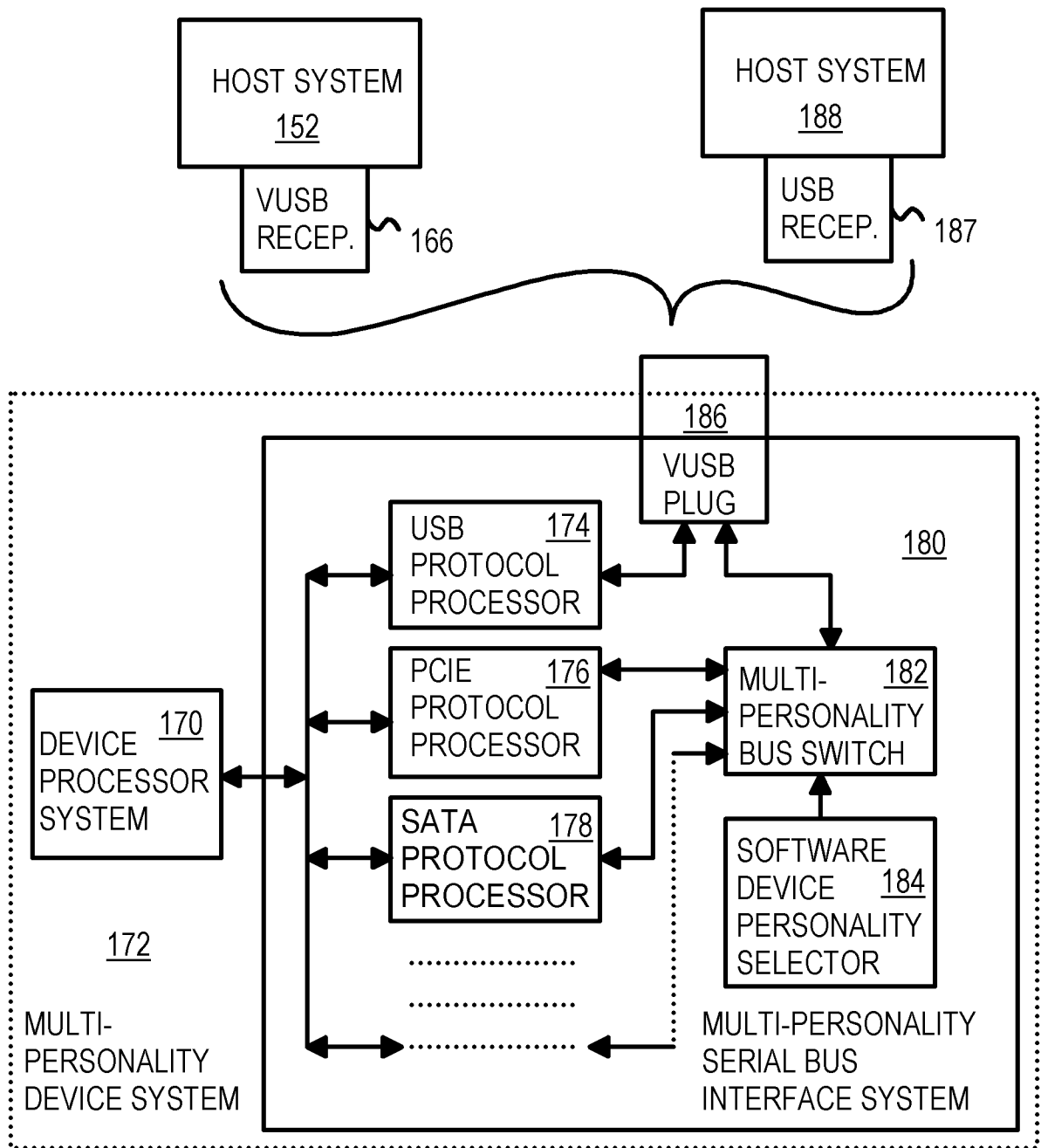


FIG. 7B

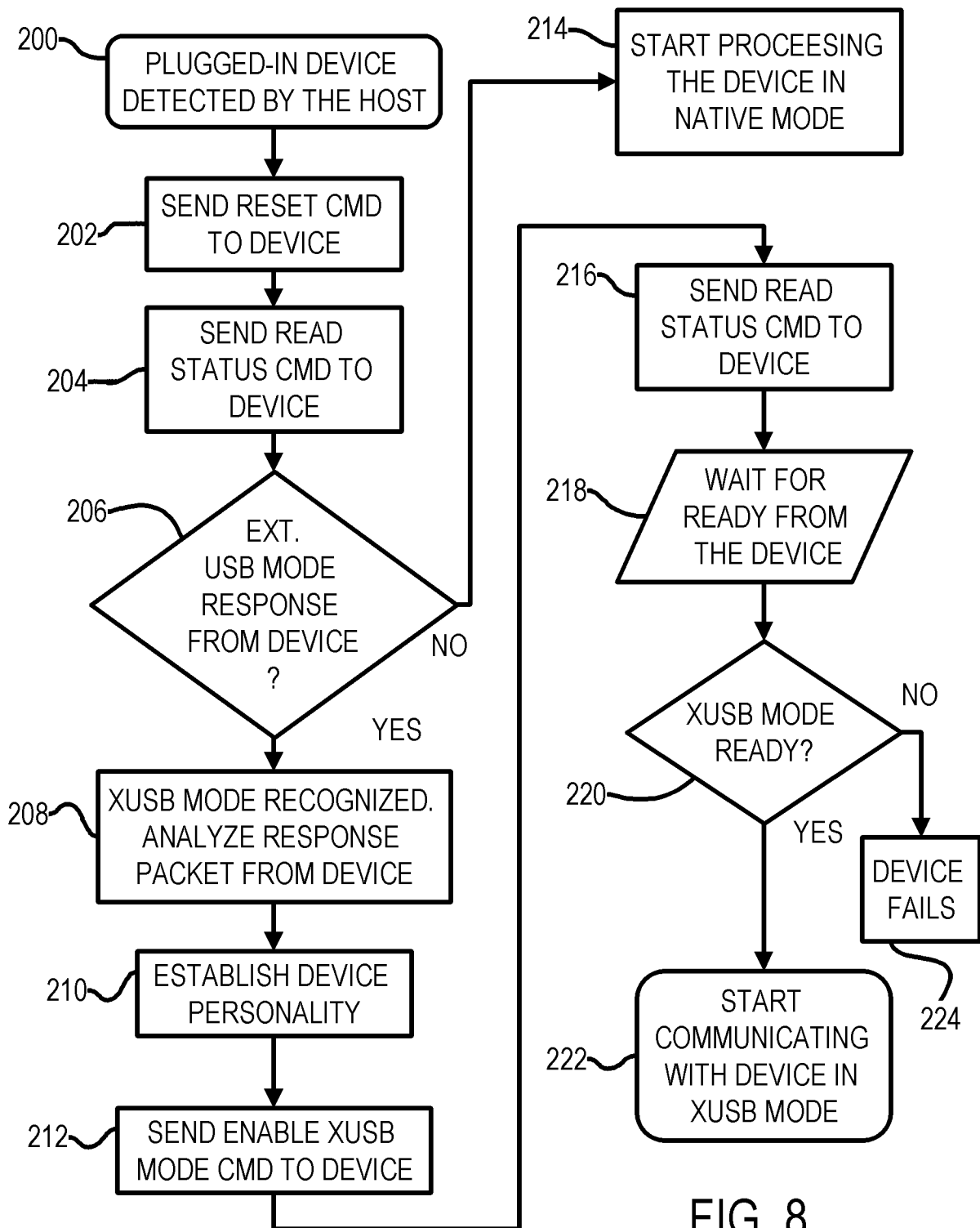
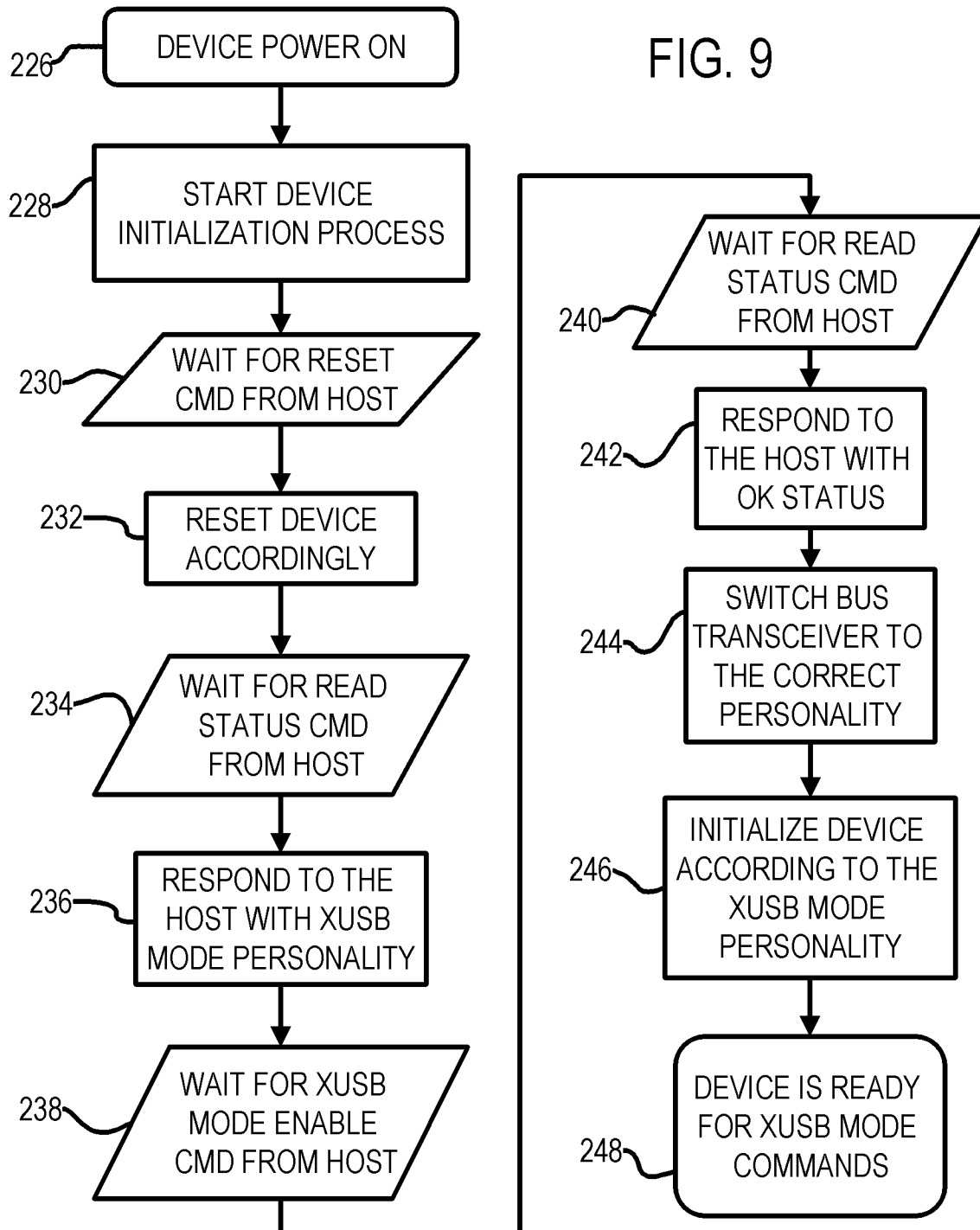


FIG. 8



SIDE	PIN-OUT	USB	PCI-EXPRESS	S-ATA	IEEE 1394
A	1	5V	5V	5V	5V
A	2	D-	D-	D-	D-
A	3	D+	D+	D+	D+
A	4	GND	GND	GND	GND
B	1		3.3V	3.3V	3.3V
B	2		1.5V	N/C	N/C
B	3		PETn	T-	TPB*
B	4		PETp	T+	TPB
B	5		GND	GND	GND
B	6		PERn	R-	TPA*
B	7		PERp	R+	TPA
B	8		N/C	12V	12V

FIG. 10